

L Number	Hits	Search Text	DB	Time stamp
1	7880	<b>associative near6 (cache memory storage)</b>	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/08/20 17:04
2	119	<b>(controller logic circuit) same multiplexer same (associative near6 (cache memory storage))</b>	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/08/20 17:21
3	43	<b>(read\$3 load\$3) near4 (buffer register) and ((controller logic circuit) same multiplexer same (associative near6 (cache memory storage)))</b>	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/08/20 17:21
5	14	<b>(concurrently simultaneous parallel ("SAME" adj3 time)) same ((controller logic circuit) same multiplexer same (associative near6 (cache memory storage)))</b>	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/08/20 17:20
6	84	<b>(concurrently simultaneous parallel ("SAME" adj3 time)) and ((controller logic circuit) same multiplexer same (associative near6 (cache memory storage)))</b>	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/08/20 17:21
7	513756	<b>(controller logic circuit) same (concurrently simultaneous parallel ("SAME" adj3 time))</b>	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/08/20 17:21
8	38490	<b>(controller logic circuit) same (concurrently simultaneous parallel ("SAME" adj3 time)) same enabl\$3</b>	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/08/20 17:22
9	272	<b>(read\$3 load\$3) near4 (buffer register) and (associative near6 (cache memory storage)) and ((controller logic circuit) same (concurrently simultaneous parallel ("SAME" adj3 time)) same enabl\$3) and multiplexer</b>	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/08/20 17:23
10	172	<b>(read\$3 load\$3) near4 (buffer register) and (associative near6 (cache memory storage)) and ((controller logic circuit) same (concurrently simultaneous parallel ("SAME" adj3 time)) same enabl\$3) and (multiplexer with (input\$4 output\$4))</b>	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/08/20 17:24
11	60	<b>711/\$.ccls. and ((read\$3 load\$3) near4 (buffer register) and (associative near6 (cache memory storage)) and ((controller logic circuit) same (concurrently simultaneous parallel ("SAME" adj3 time)) same enabl\$3) and (multiplexer with (input\$4 output\$4)))</b>	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/08/20 17:24